

CELADON

STANDARD CODE 6A – SE56A Low Volume Remote Control

IC NO. : _____

NECFORMAT:FULL REPEAT

Custom Code: C020

Custom Code: C0 C1 C2 C3 C4 C5 C6 C7 Custom Code : C0 C1 C2 C3 C4 C5 C6 C7

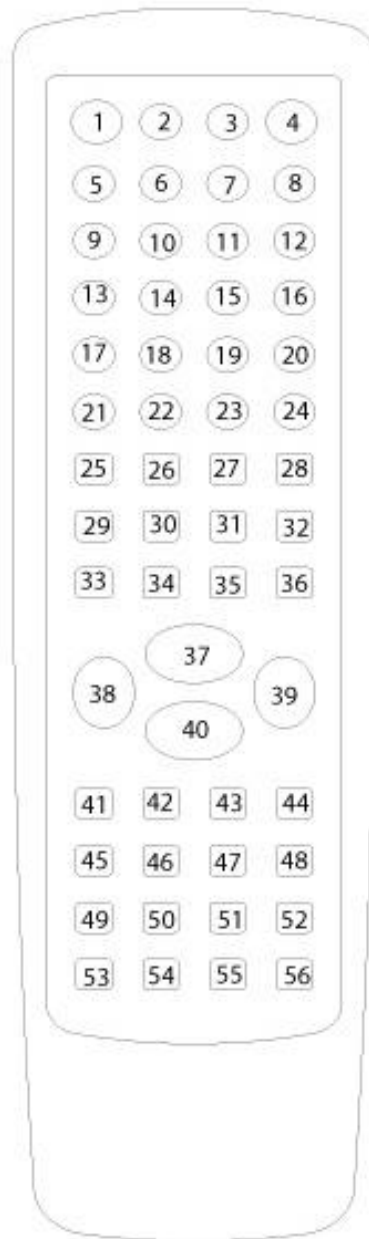
BIN CODE: 0 0 0 0 0 0 1 1 1 0 0 0 1 0 0 1

HEX CODE: C 0 2 0

Key No.	Hex	Function	Data							
			D0	D1	D2	D3	D4	D5	D6	D7
1	9Ah		0	1	0	1	1	0	0	1
2	14h		0	0	1	0	1	0	0	0
3	A4h		0	0	1	0	0	1	0	1
4	A2h		0	1	0	0	0	1	0	1
5	EAh		0	1	0	1	0	1	1	1
6	9Ch		0	0	1	1	1	0	0	1
7	8Ah		0	1	0	1	0	0	0	1
8	CCh		0	0	1	1	0	0	1	1
9	6Ah		0	1	0	1	0	1	1	0
10	1Ch		0	0	1	1	1	0	0	0
11	0Ah		0	1	0	1	0	0	0	0
12	4Ch		0	0	1	1	0	0	1	0
13	AAh		0	1	0	1	0	1	0	1
14	ECh		0	0	1	1	0	1	1	1
15	92h		0	1	0	0	1	0	0	1
16	8Ch		0	0	1	1	0	0	0	1
17	2Ah		0	1	0	1	0	1	0	0
18	6Ch		0	0	1	1	0	1	1	0
19	12h		0	1	0	0	1	0	0	0
20	0Ch		0	0	1	1	0	0	0	0
21	E4h		0	0	1	0	0	1	1	1
22	94h		0	0	1	0	1	0	0	1
23	84h		0	0	1	0	0	0	0	1
24	C4h		0	0	1	0	0	0	1	1
25	1Ah		0	1	0	1	1	0	0	0
26	02h		0	1	0	0	0	0	0	0
27	62h		0	1	0	0	0	1	1	0
28	4Ah		0	1	0	1	0	0	1	1
29	2Ch		0	0	1	1	0	1	0	0
30	60h		0	0	0	0	0	1	1	0
31	10h		0	0	0	0	1	0	0	0
32	A0h		0	0	0	0	0	1	0	1
33	E0h		0	0	0	0	0	1	1	1

Key No.	Hex	Function	Data							
			D0	D1	D2	D3	D4	D5	D6	D7
34	90h		0	0	0	0	1	0	0	1
35	80h		0	0	0	0	0	0	0	1
36	C0h		0	0	0	0	0	0	1	1
37	CAh		0	1	0	1	0	0	1	1
38	42h		0	1	0	0	0	0	1	0
39	AC h		0	0	1	1	0	1	0	1
40	22h		0	1	0	0	0	1	0	0
41	00h		0	0	0	0	0	0	0	0
42	40h		0	0	0	0	0	0	1	0
43	20h		0	0	0	0	0	1	0	0
44	64h		0	0	1	0	0	1	1	0
45	14h		0	0	1	0	1	0	0	0
46	A4h		0	0	1	0	0	1	0	1
47	E4h		0	0	1	0	0	1	1	1
48	94h		0	0	1	0	1	0	0	1
49	84h		0	0	1	0	0	0	0	1
50	C4h		0	0	1	0	0	0	1	1
51	04h		0	0	1	0	0	0	0	0
52	44h		0	0	1	0	0	0	1	0
53	24h		0	0	1	0	0	1	0	0
54	68h		0	0	0	1	0	1	1	0
55	18h		0	0	0	1	1	0	0	0
56	A8h		0	0	0	1	0	1	0	1

SE-56



SE-56

Up to 56 Keys
Dimensions (mm.) 200 x 50 x 26
Rubber Keypad
Keypad Inlay
Batteries: 2 x AAA
LED Indicator

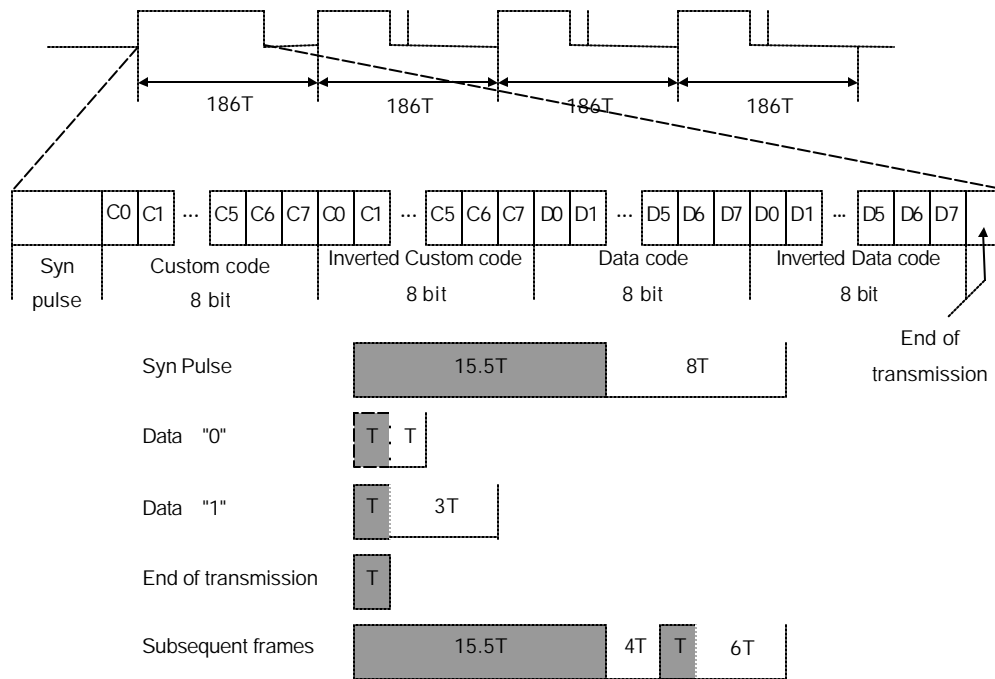
NEC Protocol Definition

The modulated carrier is usually derived from 455kHz and is 1/12 of the frequency with 1/3 duty cycle.

When data are transmitted repeatedly, the frame cycle is 107.9ms or 186 period.

A frame consists of a syn pulse, an eight-bit custom code, an eight-bit inverted custom code, an eight-bit data code and an eight-bit inverted data code.

The timing definitions of the output code waveform are shown below.



Data Item	Time (sec.)	Time (no. of period)
Syn pulse on time	8.993ms	15.5T
Syn pulse off time	4.642ms	8T
Syn pulse off time (subsequent frame)	2.321ms	4T
Data on time (0)	0.561ms	T
Data off time (0)	0.598ms	T
Data on time (1)	0.561ms	T
Data off time (1)	1.7582.63ms	3T
Data period (0)	1.160ms	2T
Data period (1)	2.321ms	4T
Frame output cycle	107.9ms	186T

Where T=0.58ms